

Notice of References Cited

Application/Control No.

09/531,350

Applicant(s)/Patent Under
Reexamination
GREENBERG, STEVEN S.

Examiner

Dwin M Craig

Art Unit

2123

Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,466,898	10-2002	Chan, Terence	703/17
	B	US-6,324,495	11-2001	Steinman, Jeffrey S.	703/17
	C	US-6,110,217	08-2000	Kazmierski et al.	703/14
	D	US-5,794,005	08-1998	Steinman, Jeffrey S.	703/17
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)				
	U	Premal Buch, Ernest S. Kuh, "SYMPHONY: A Fast Signal Simulator for BiCMOS Analog/Digital Circuits", IEEE 1996, Pages 403-407.				
	V	Ernst G. Ulrich, "NON-INTEGRAL EVENT TIMING FOR DIGITAL LOGIC SIMULATION", ACM 1976, Pages 61-67.				
	W					
	X					

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.